

## United States Patent and Trademark Office

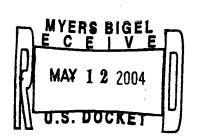


Page 1 of 2

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.usplus.gov

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/780,245	02/18/2004	2812	770	5649-1200	_ <del></del> 7	15	1

20792 MYERS BIGEL SIBLEY & SAJOVEC PO BOX 37428 RALEIGH, NC 27627



**CONFIRMATION NO. 9654** 



Date Mailed: 05/10/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Chang-hyun Park, Kyungki-do, KOREA, REPUBLIC OF; Young-gun Ko, Kyungki-do, KOREA, REPUBLIC OF; Hee-sung Kang, Kyungki-do, KOREA, REPUBLIC OF; Sang-jin Lee, Seoul, KOREA, REPUBLIC OF;

Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 10-2003-10323 02/19/2003

If Required, Foreign Filing License Granted: 05/07/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

05-12-04 A09:13 IN

Title

Methods of fabricating MOS field effect transistors with pocket regions